

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A memory tag ~~having~~ comprising:
a non-volatile memory in which in use data is stored[[,]];
an antenna coil and power supply circuit configured such that in use the
memory tag is powered by inductive coupling[[,]]; ~~wherein the memory tag also~~
~~includes~~
a sensor for receipt of transmitted light carrying input signals; ~~and~~
a modulation circuit for overlay of output signals onto the power supply
circuit; and
a processor for processing of the received input signals and the output signals;
~~and~~
~~a modulation circuit for overlay of output signals onto the power supply~~
~~circuit.~~
2. (Original) A memory tag according to claim 1 wherein output signals are sent
via the inductive coupling in response to input signals received optically.
3. (Previously Presented) A memory tag according to claim 1 wherein the input
signals are data and/or control signals.
4. (Previously Presented) A memory tag according to claim 1 wherein the output
signals are indicative of the data stored in the memory.
5. (Original) A memory tag according to claim 1 wherein the processor further
controls the memory and the sensor.
6. (Original) A memory tag according to claim 1 wherein the sensor is a CMOS
light sensor.

7. (Currently Amended) A memory tag according to claim 6 wherein [[it]] the memory tag is implemented on a single semiconductor chip.
8. (Currently Amended) A read/write device, for communication with a memory tag according to ~~anyone of the preceding claims~~ claim 1, having a signal generator, an antenna coil and a power supply circuit for powering the memory tag in use by inductive coupling, and wherein the read/write device further includes a light emitter for emission of the light carrying the input signals to the memory tag, and a demodulation circuit for retrieval of the output signals from the inductive coupling.
9. (Original) A read/write device according to claim 8 wherein it further includes a processor for control of the light emitter.
10. (Currently Amended) A method of operating a wireless memory tag comprising:
 - powering the memory tag by inductive coupling; and
 - ~~communicating with the memory tag by transmitting control and/or data signals to the~~
 - memory tag using optical signals; ~~and~~
 - receiving output signals from the memory tag as modulation of the inductive coupling; and
 - processing both the input signals and the output signals by a processor of the memory tag.
11. (New) A memory tag comprising:
 - a non-volatile memory in which in use data is stored;
 - an antenna coil and power supply circuit configured such that in use the memory tag is powered by inductive coupling;
 - a sensor for receipt of transmitted light carrying input signals;
 - a modulation circuit for overlay of output signals onto the power supply circuit; and
 - a processor for processing of the received input signals.